

Notice of Allowability

Application No.

10/803,965

Examiner

Kiesha L. Rose

Applicant(s)

KOYAMA ET AL.

Art Unit

2822

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the RCE filed 7/29/05.
2. ☒ The allowed claim(s) is/are 10-63.
3. ☒ The drawings filed on 19 March 2004 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 7/29/05
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.


AMIR ZARABIAN
SUPERVISOR IN CHARGE
TECHNICAL CENTER

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with John Hayden on August 23, 2005.

The application has been amended as follows:

Title

Semiconductor Device

Claims

10. A semiconductor device, comprising: a first wiring line formed over a substrate; the first wiring line comprising at least a first portion and second portion; **where second portion is not directly connected to the first portion**; a second wiring line formed over first wiring line with an insulating film interposed therebetween, wherein the first wiring line is formed on a same layer as a gate electrode of a thin film transistor, wherein the first wiring line and second wiring line extend in parallel with each other, wherein the first portion of the first wiring line and the second wiring line are electrically connected with each other via a plurality of contact holes opened in the insulating film, and wherein the second portion of the first wiring line and second wiring line are

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electrically connected with each other via a plurality of contact holes opened in the insulating film.

40. A semiconductor device, comprising: a first wiring line formed over a substrate; the first wiring line comprising at least a first portion and second portion; **where second portion is not directly connected to the first portion**; a second wiring line formed over first wiring line with an insulating film interposed therebetween, wherein one of the first wiring line and the second wiring line is formed on a same layer as a source or a drain electrode of a thin film transistor, wherein the first wiring line and second wiring line extend in parallel with each other, wherein the first portion of the first wiring line and the second wiring line are electrically connected with each other via a plurality of contact holes opened in the insulating film, and wherein the second portion of the first wiring line and second wiring line are electrically connected with each other via a plurality of contact holes opened in the insulating film.

Allowable Subject Matter

Claims 16-63 are allowed.

The following is an examiner's statement of reasons for allowance: Claims 10-15,40-45,51,57 and 63 are allowable because prior art does not disclose alone or in combination along with the limitations of the independent claims such as a first wiring line comprising a first portion and second portion, where the second portion is not directly connected to the first portion, wherein the first portion of the first wiring line and second wiring line are electrically connected with each other via a plurality of contact

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holes opened in the insulating film and wherein the second portion of the first wiring line and second wiring line are electrically connected with each other via a plurality of contact holes opened in the insulating film.

Claims 16-39, 46-50, 52-56 and 58-62 are allowable because prior art does not disclose alone or in combination along with the limitations of the independent claims such as a third wiring line formed on a same surface as the first wiring line and extending between the first and second portions of the wiring line wherein the first portion of the first wiring line and second wiring line are electrically connected with each other via a plurality of contact holes opened in the insulating film and wherein the second portion of the first wiring line and second wiring line are electrically connected with each other via a plurality of contact holes opened in the insulating film.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kiesha L. Rose whose telephone number is 571-272-1844. The examiner can normally be reached on M-F 8:30-6:00 off 2nd Mondays.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


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